

FIG. 2A

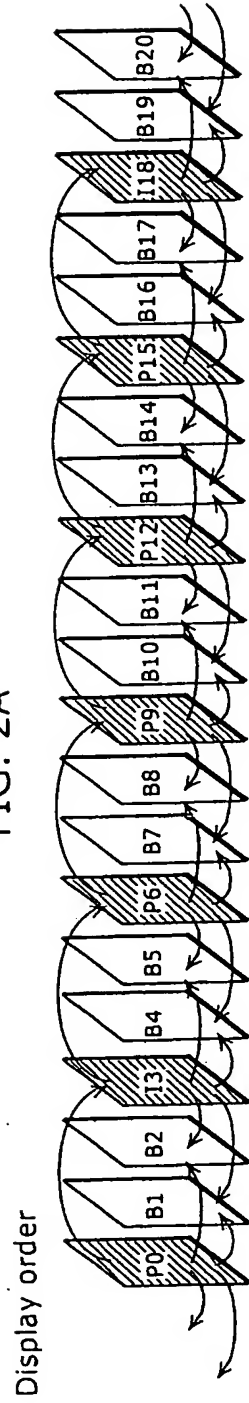


FIG. 2B

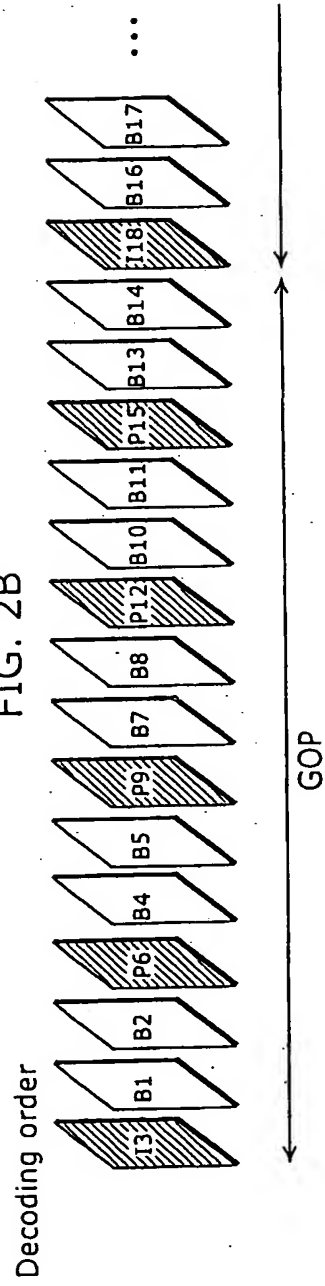


FIG. 3A

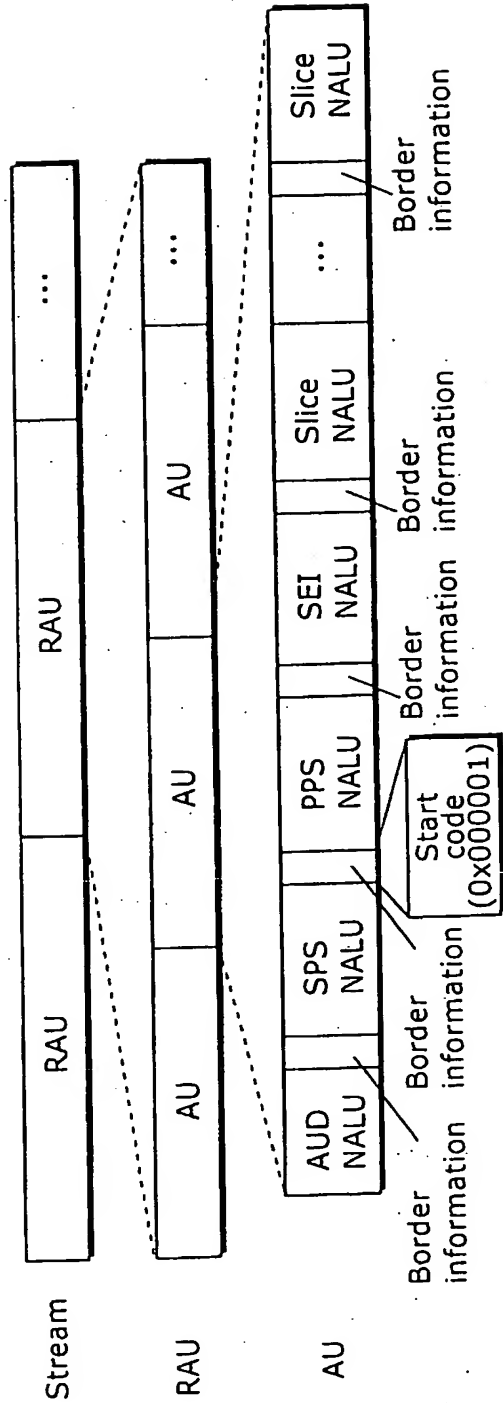


FIG. 3B

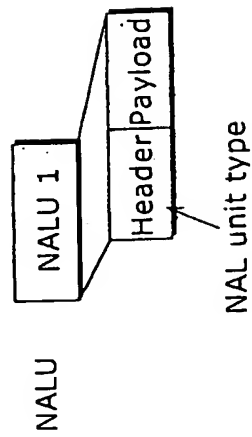


FIG. 4

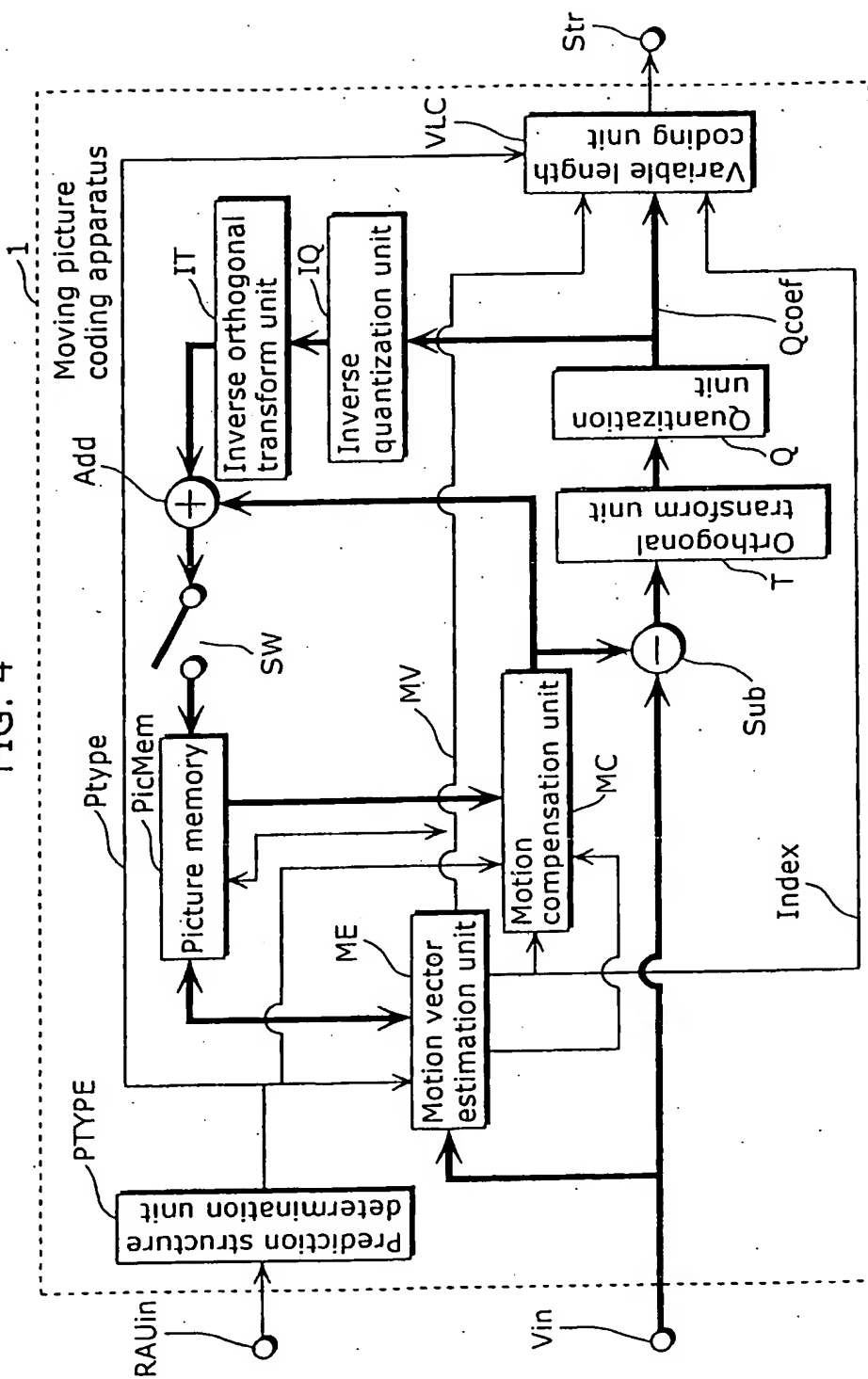


FIG. 5

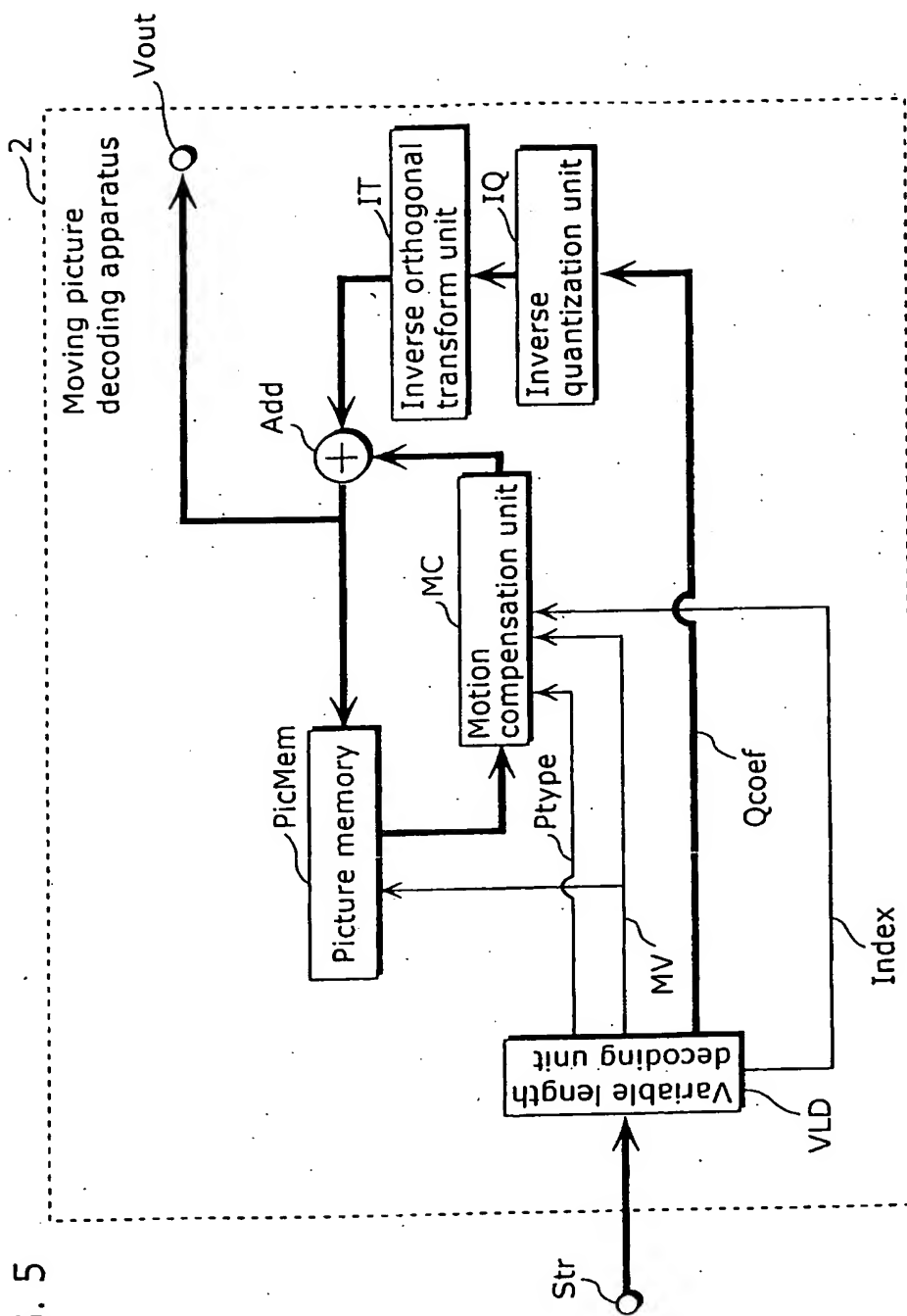


FIG. 7

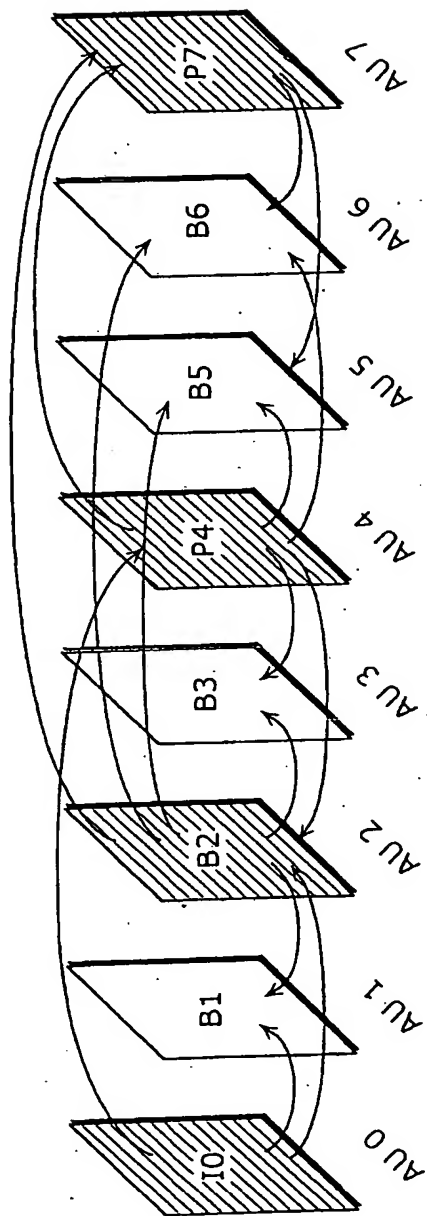


FIG. 8A

The diagram illustrates a video stream structure. It shows a sequence of NAL (Network Abstraction Layer) units. The top part of the diagram shows a sequence of AU (Access Unit) NAL units, which are grouped into two RAU (Random Access Unit) sections. The bottom part of the diagram shows a sequence of NAL units, including AUD NALU, SPS NALU, PPS NALU, Trick-play information NALU, and Slice NALU. A line connects the first AU NALU to the first Slice NALU, indicating a mapping or relationship between the two.

Trick-play information SEI message

AUD NALU	SPS NALU	PPS NALU	SEI NALU	Slice NALU	...	Slice NALU
----------	----------	----------	----------	------------	-----	------------

Trick-play information SEI message

FIG. 9A

Display order

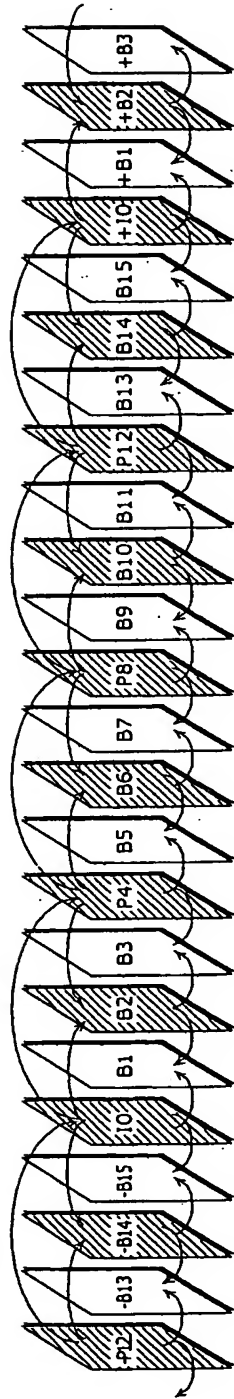


FIG. 9B

Decoding
order

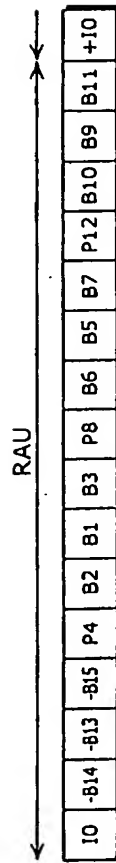


FIG. 9C

Double
-speed

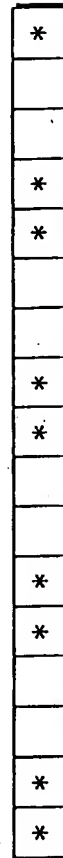


FIG. 9D

Quadruple
-speed



FIG. 10A

Display order

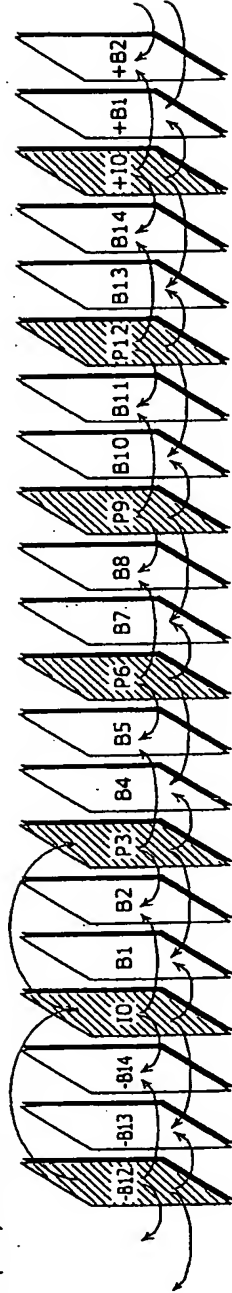


FIG. 10B

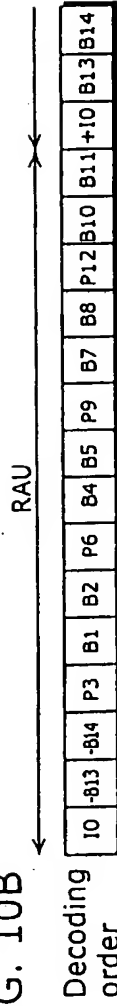


FIG. 10C

1.5-times speed

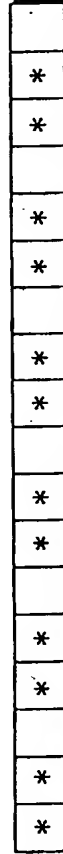


FIG. 10D

Triple-speed



FIG. 11A

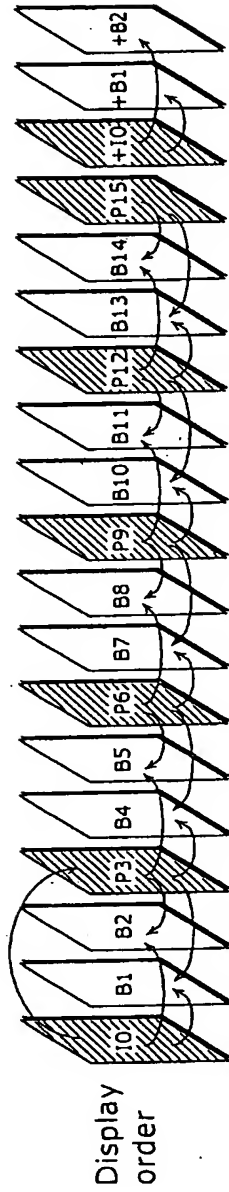


FIG. 11B.

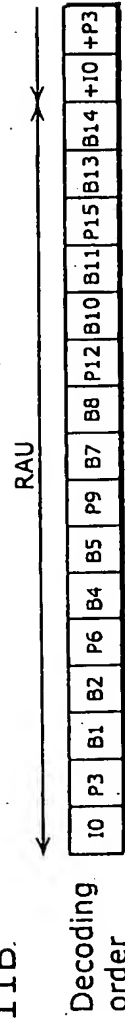


FIG. 11C



FIG. 12A

Display order

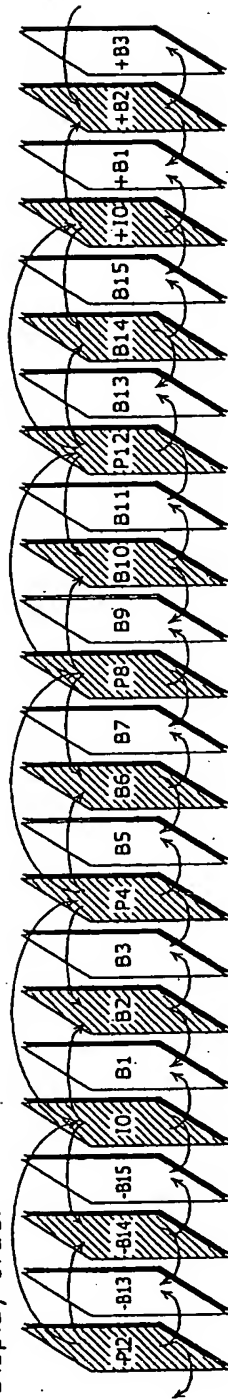


FIG. 12B

Decoding
order

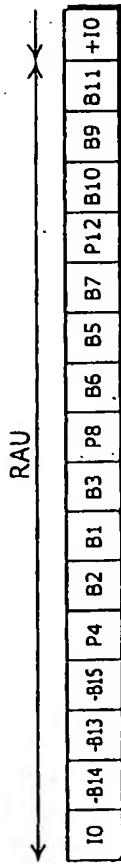


FIG. 12C

Reference AUs

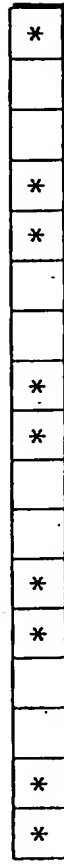


FIG. 12D

Double-speed



FIG. 12E

Order of AUs

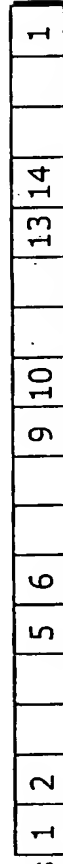
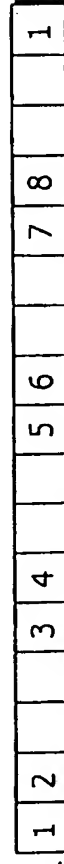


FIG. 12F

Order of
reference AUs



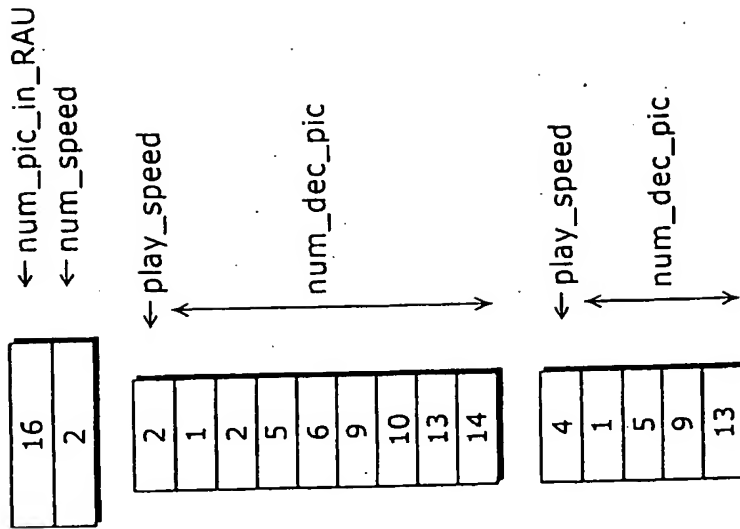
```

Variable Speed Play {
    num_pic_in_RAU;
    num_speed;
    for (i=0; i < num_speed; i++) {
        play_speed;
        num_dec_pic;
        for (j=0; j < num_dec_pic; j++) {
            dec_pic;
        }
    }
}

```

Syntax example

FIG. 13A



Data storage unit

FIG. 13B

FIG. 14

```
Variable Speed Play {  
  num_pic_in_RAU;  
  num_speed;  
  for (i=0; i < num_speed; i++) {  
    play_speed;  
    num_dec_pic;  
    pts_dts_flag;  
    for (j=0; j < num_dec_pic; j++) {  
      dec_pic;  
      if (pts_dts_flag) display_order;  
    }  
  }  
}
```

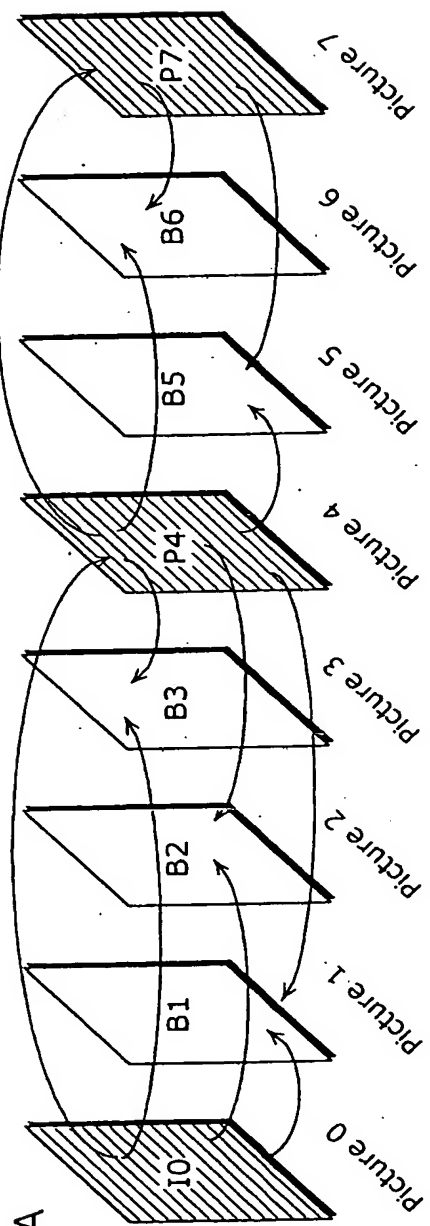


FIG. 6A

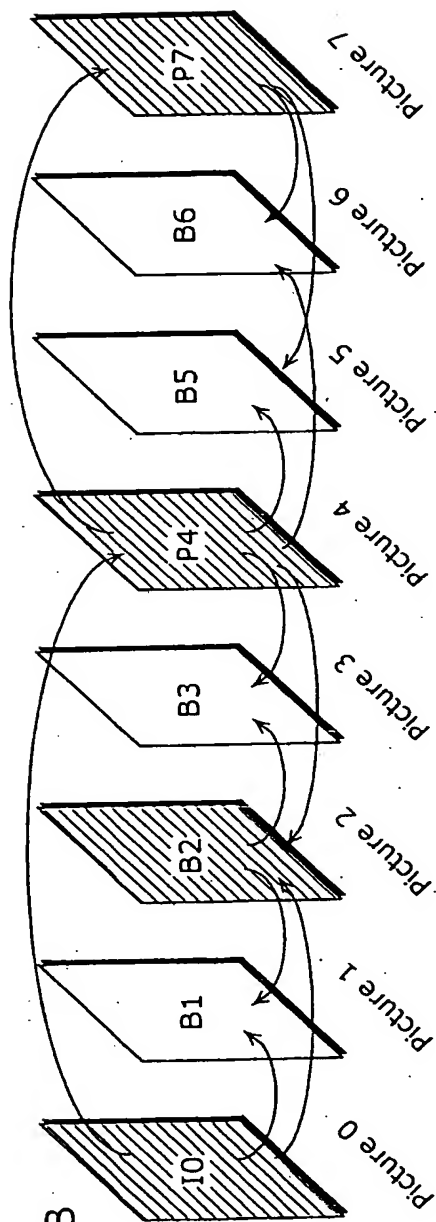


FIG. 6B

FIG. 15A

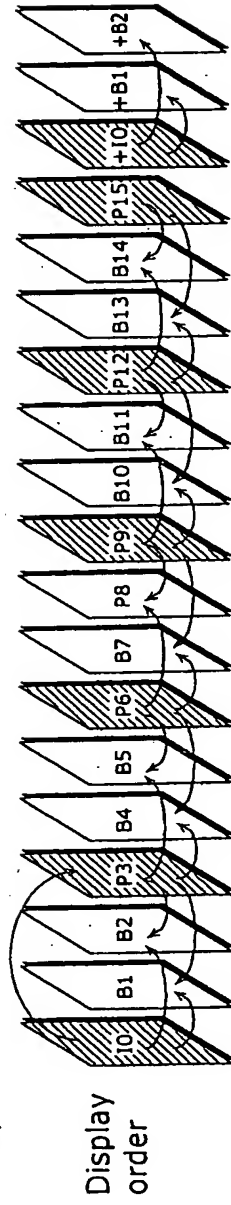


FIG. 15B

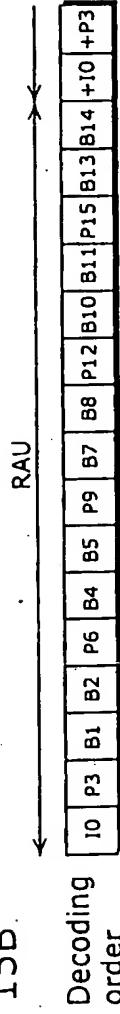


FIG. 15C

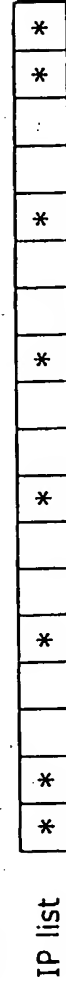


FIG. 16A

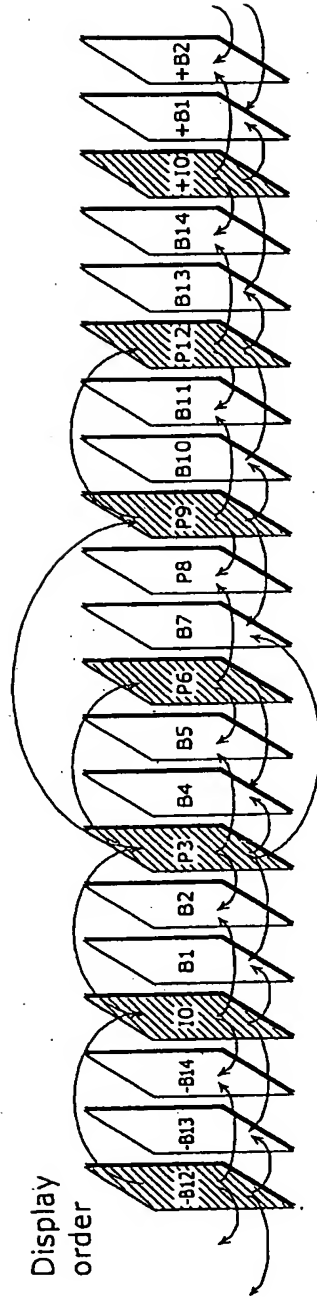


FIG. 16B

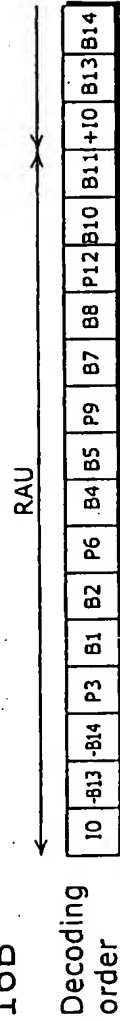


FIG. 16C

Buffer detention time

3	0	0	6	0	0	3	0	0	3	0	0	3	0	0	3	0	0
---	---	---	---	---	---	---	---	---	---	---	---	---	---	---	---	---	---

FIG. 17A

Display
order

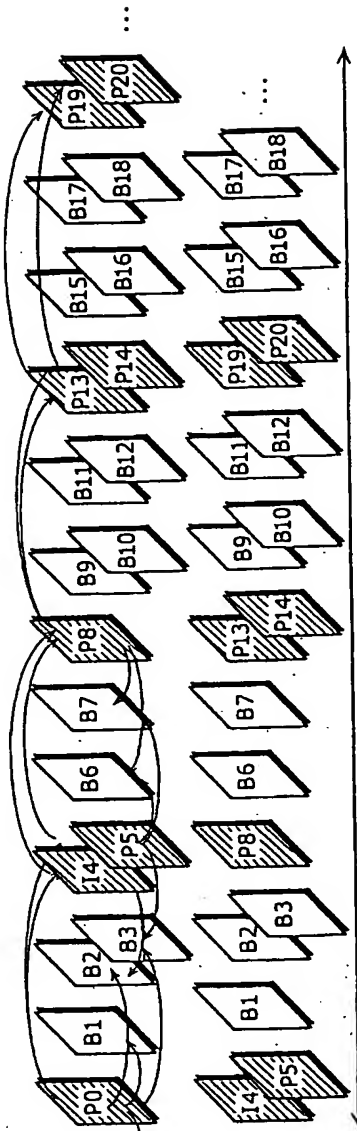


FIG. 17B

Decoding
order

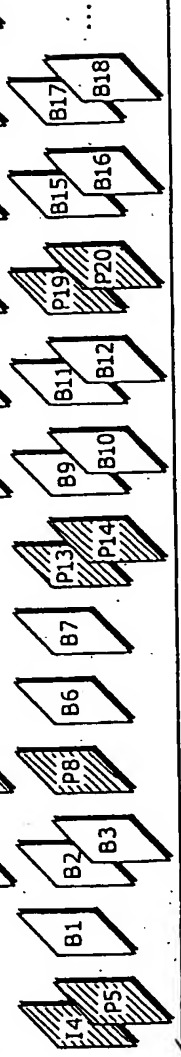


FIG. 17C

```
RAU map {
    num_AU_in_RAU;
    for (i=0; i < num_AU_in_RAU; i++) {
        frame_field_flag;
        pic_type;
    }
}
```

FIG. 17D

FIG. 17E

```
RAU map {
    num_frame_in_RAU;
    for (i=0; i < num_frame_in_RAU; i++) {
        frame_flag;
        if (frame_field_flag) frame_type;
        else field_pair_type;
    }
}
```

30	←num_AU_in_RAU	frame_flag	pic_type	←I4
0	0	0	0	←I4
0	1	0	1	←P5
1	3	1	3	←B1
0	3	0	3	←B2
0	3	0	3	←B3
1	1	1	1	←P8
1	3	1	3	←B6
1	3	1	3	←B7
0	-	0	-	←P13,P14
0	-	0	-	←B9,B10
0	-	0	-	←B11,B12
0	1	0	1	←P13
0	1	0	1	←P14
0	3	0	3	←B9
0	3	0	3	←B10
0	3	0	3	←B11
0	3	0	3	←B12
0	1	0	1	←P19
0	1	0	1	←P20
...

15	←num_frame_in_RAU	frame_flag	frame_type	field_pair_type	←I4,P5
0	-	0	-	IP	←B1
1	3	1	3	-	←B2,B3
0	-	0	-	BnBn	←P8
1	1	1	1	-	←B6
1	3	1	3	-	←B7
0	-	0	-	pp	←P13,P14
0	-	0	-	BnBn	←B9,B10
0	-	0	-	BnBn	←B11,B12
0	-	0	-	pp	←P19,P20
0	3	0	3	BnBn	←B15,B16
0	3	0	3	BnBn	←B17,B18
...

FIG. 18A

```
RAU map {  
    num_AU_in_RAU;  
    for (i=0; i < num_AU_in_RAU; i++) {  
        picture_structure;  
        picture_type;  
    }  
}
```

FIG. 18B

```
picture_structure:  Field  
                  or  Frame  
                  ⋮
```

FIG. 18C

```
picture_type:      I picture  
                  or  Reference B picture  
                  or  Non-reference B picture  
                  or  P picture  
                  ⋮
```

FIG. 19

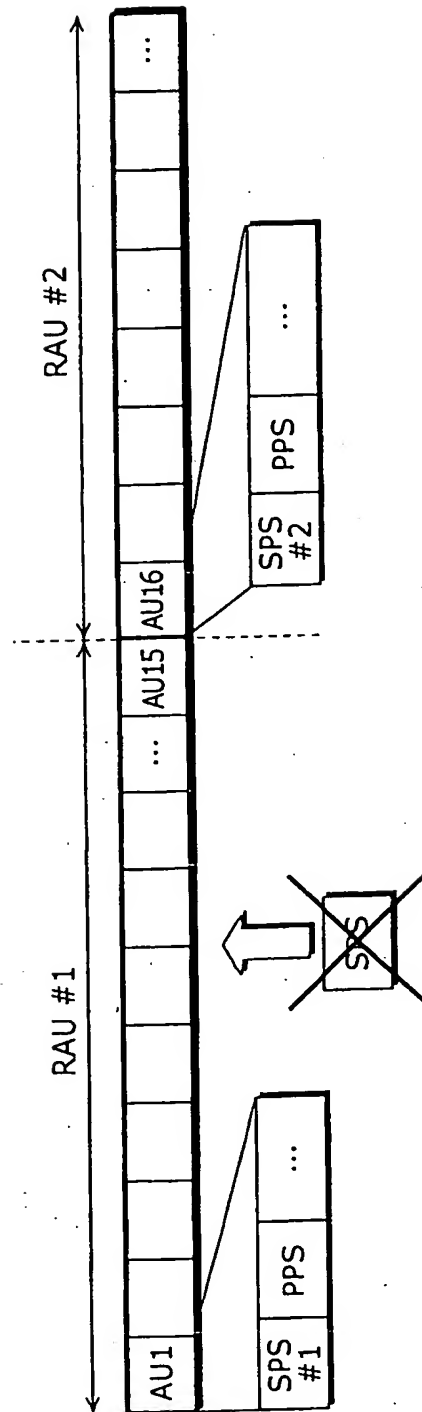


FIG. 20A

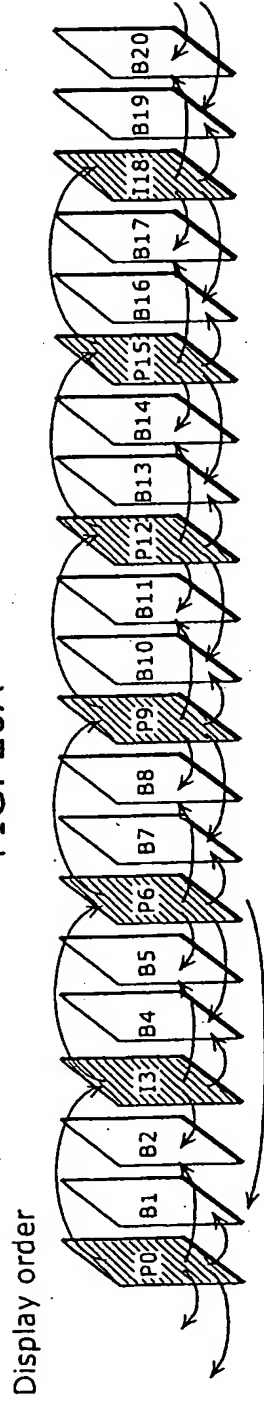
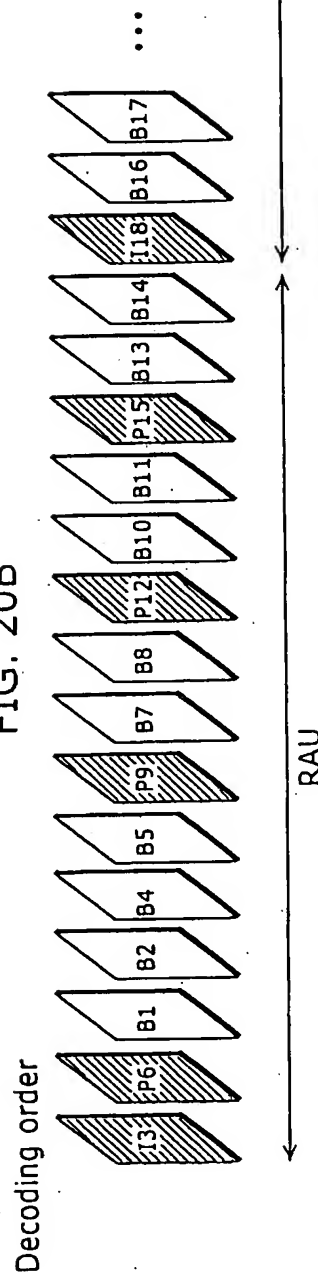


FIG. 20B



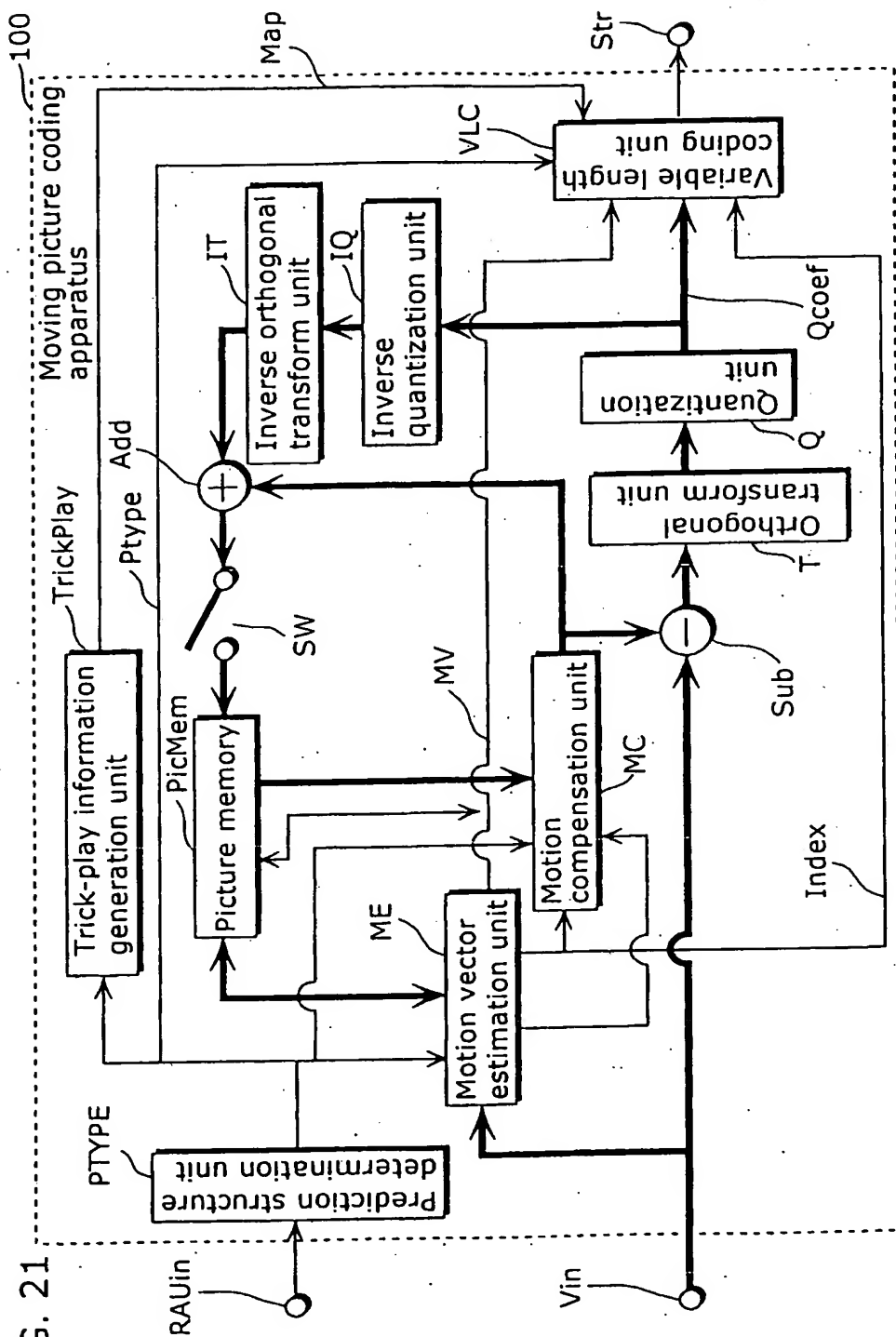


FIG. 21

FIG. 22

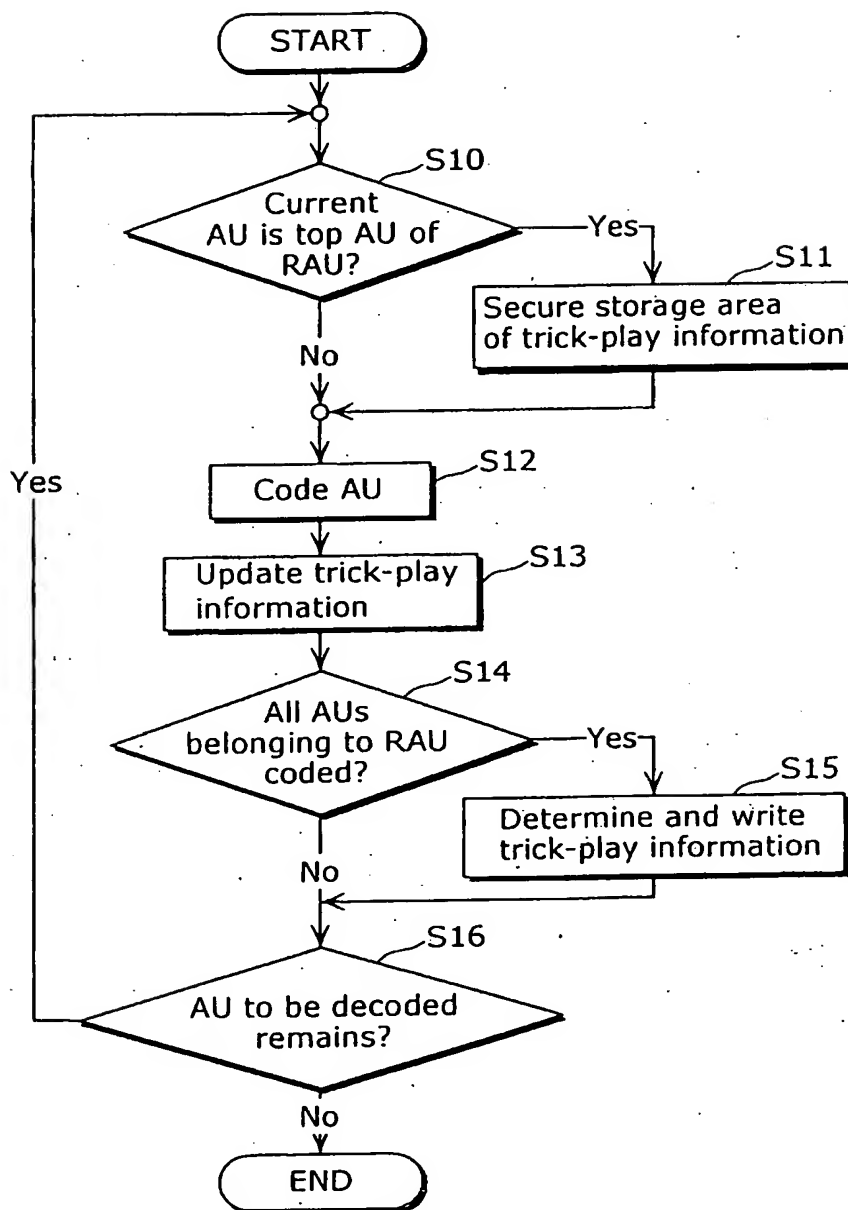


FIG. 23

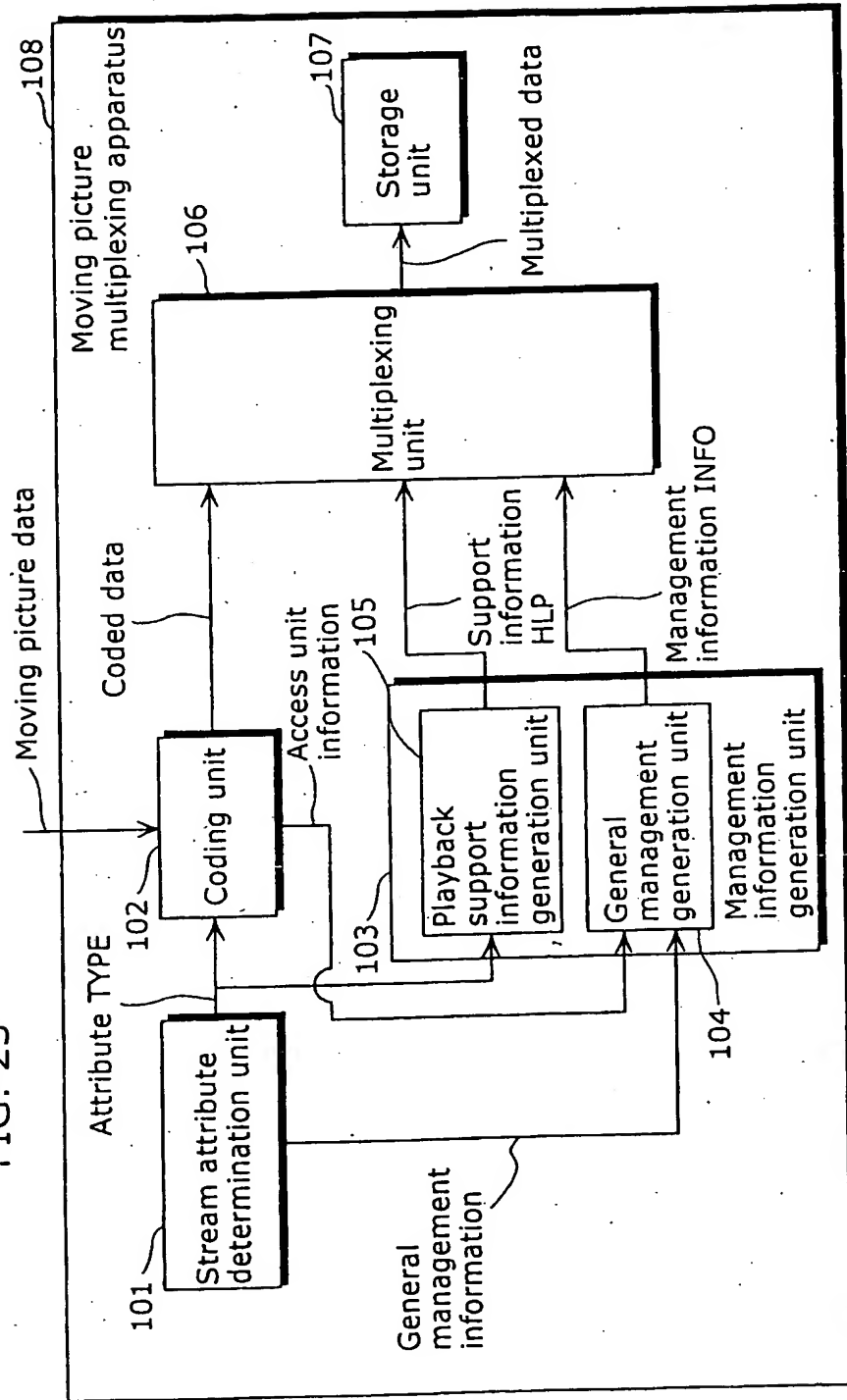


FIG. 24A

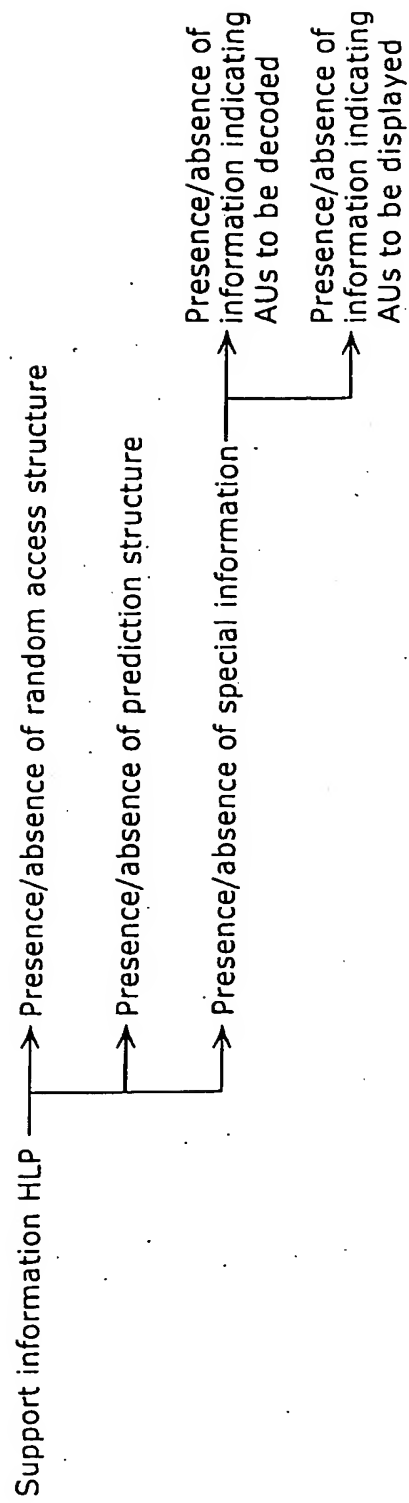
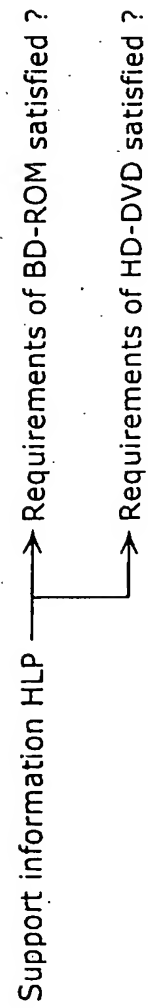


FIG. 24B



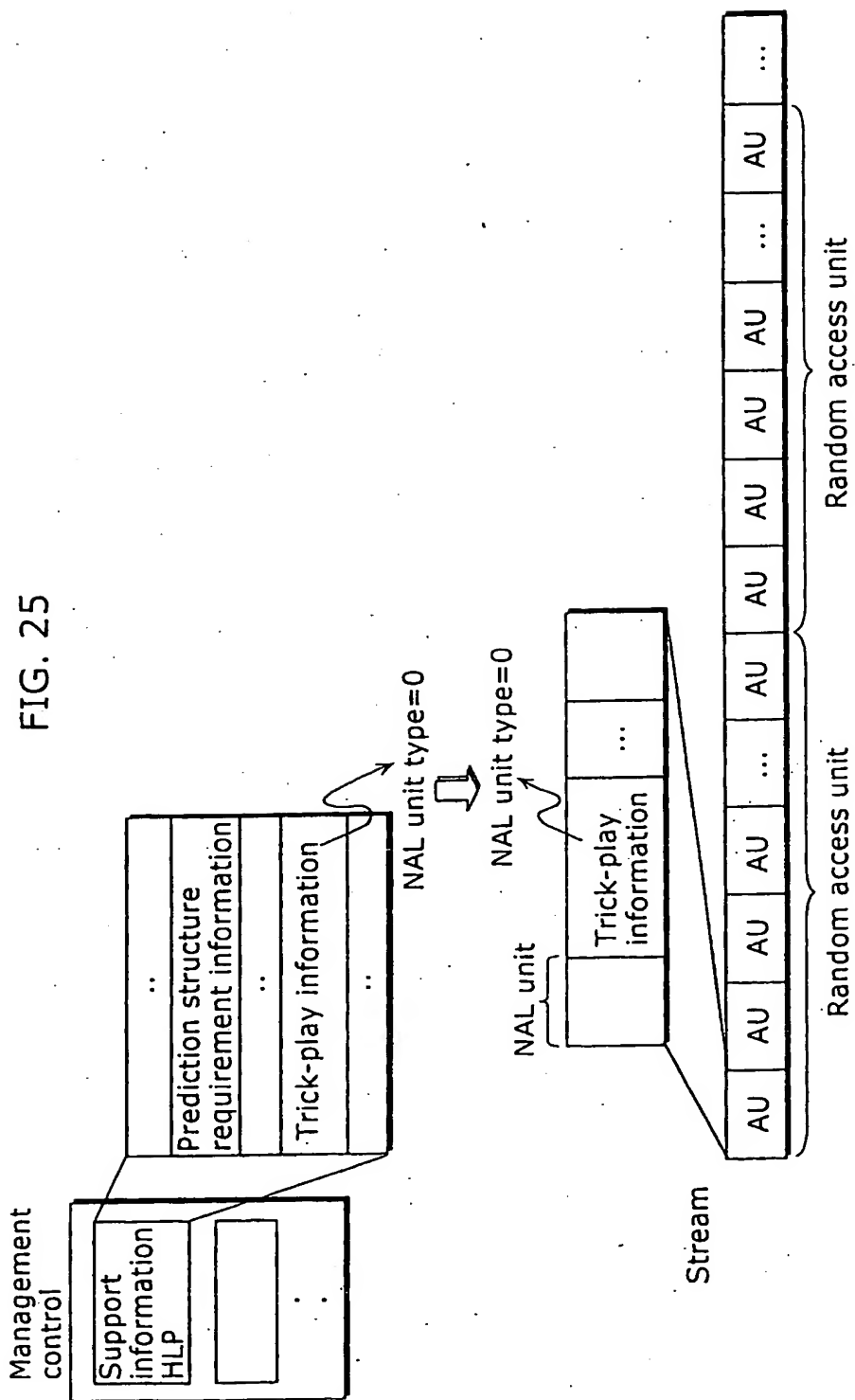


FIG. 26

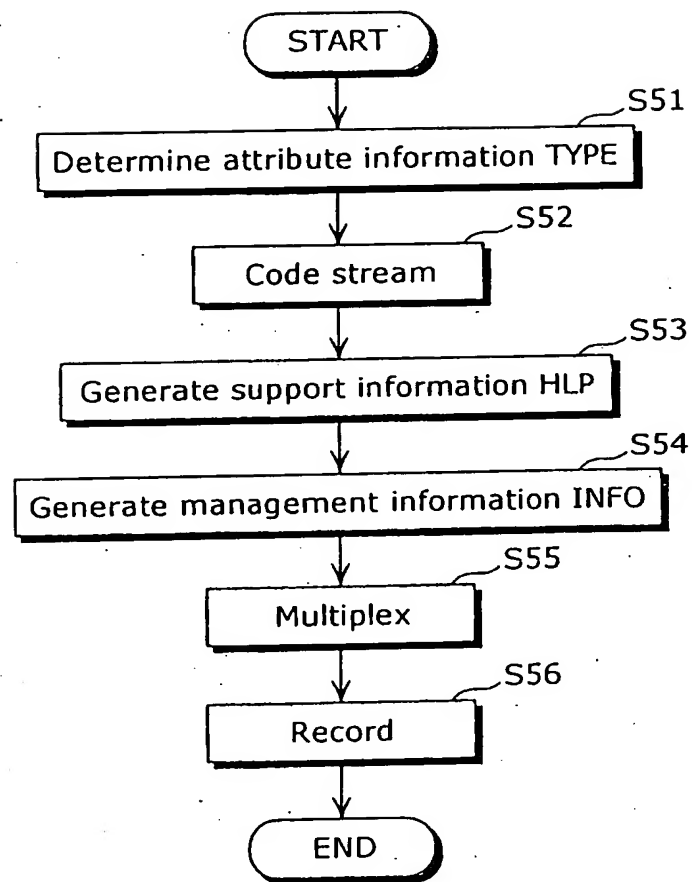


FIG. 27

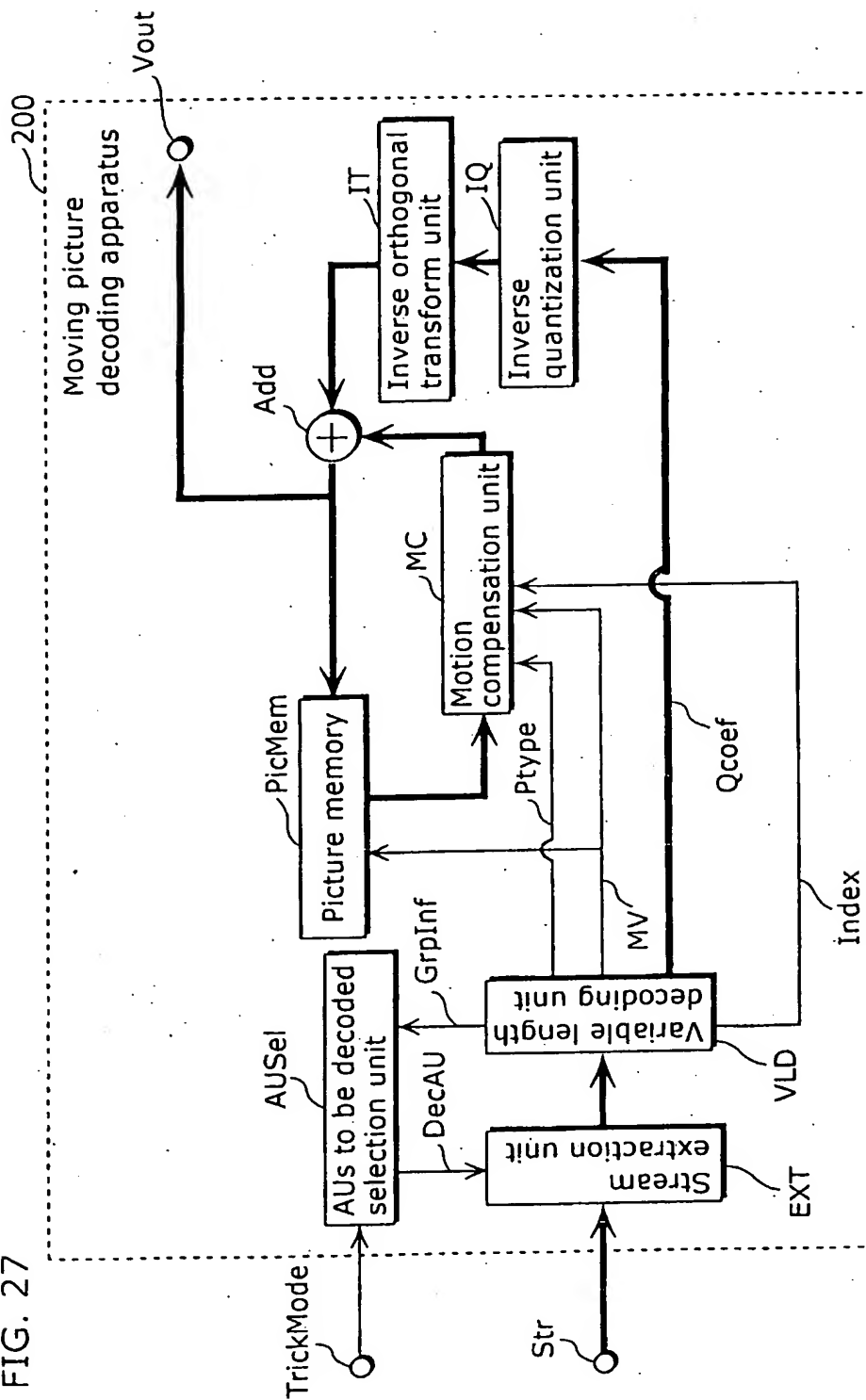


FIG. 28

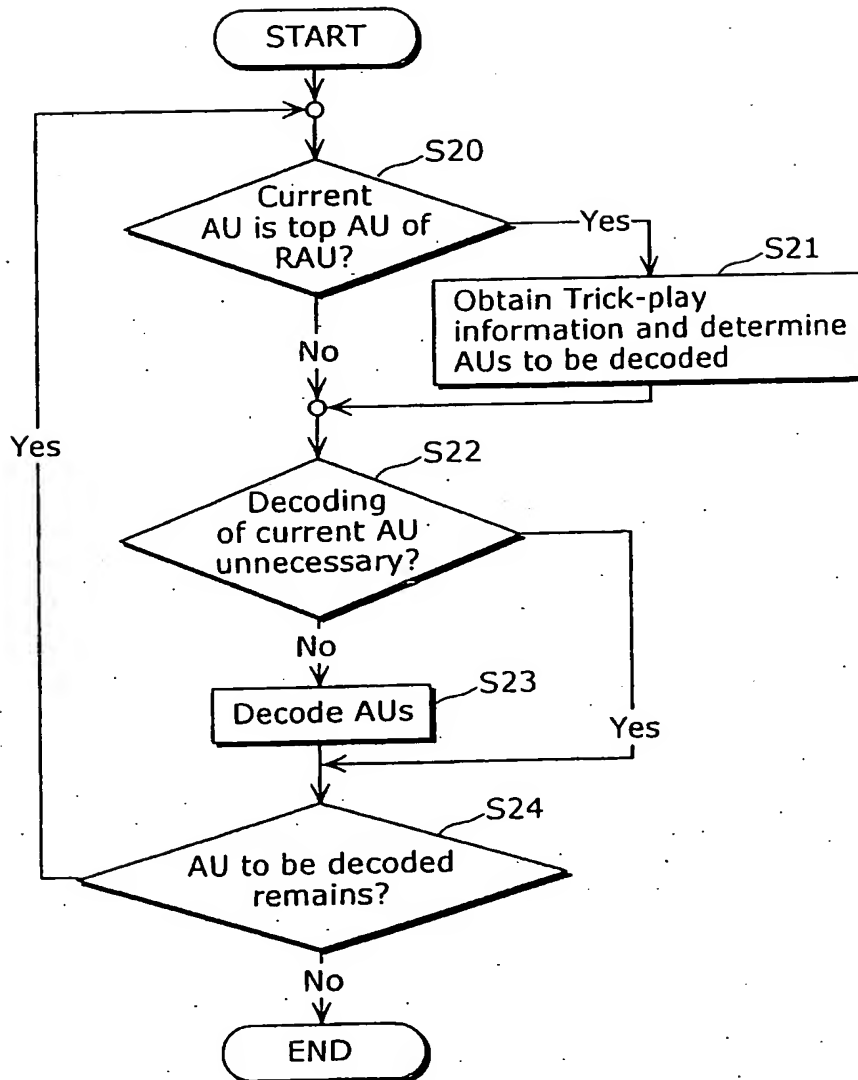


FIG. 29

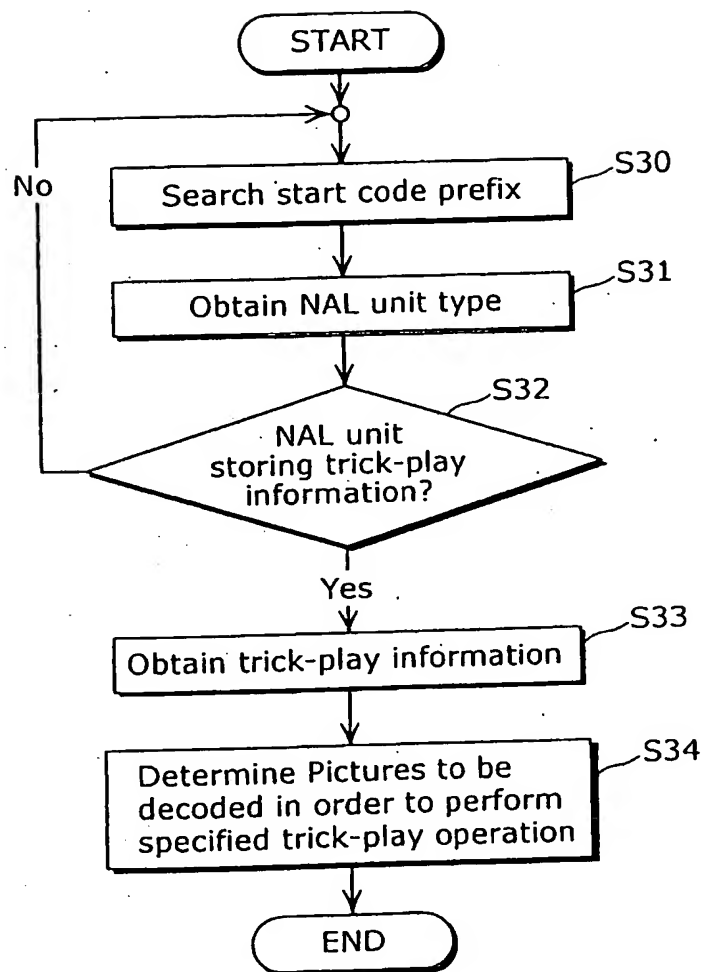


FIG. 30

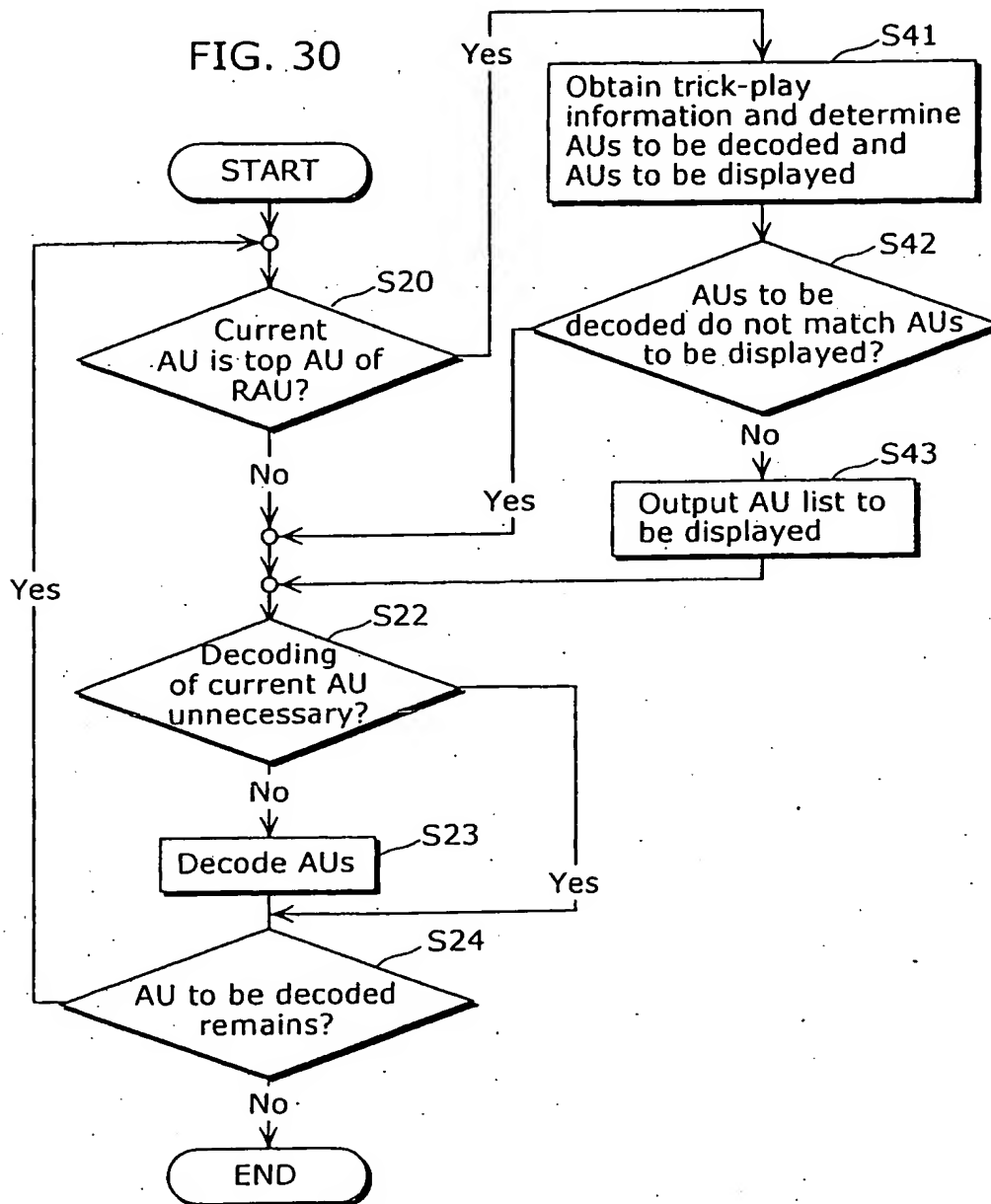


FIG. 31

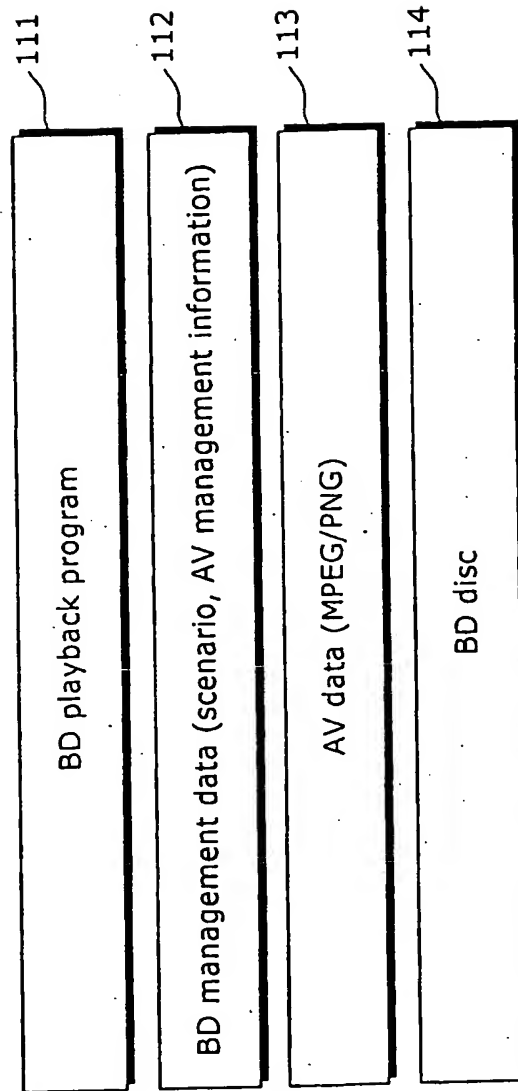


FIG. 32

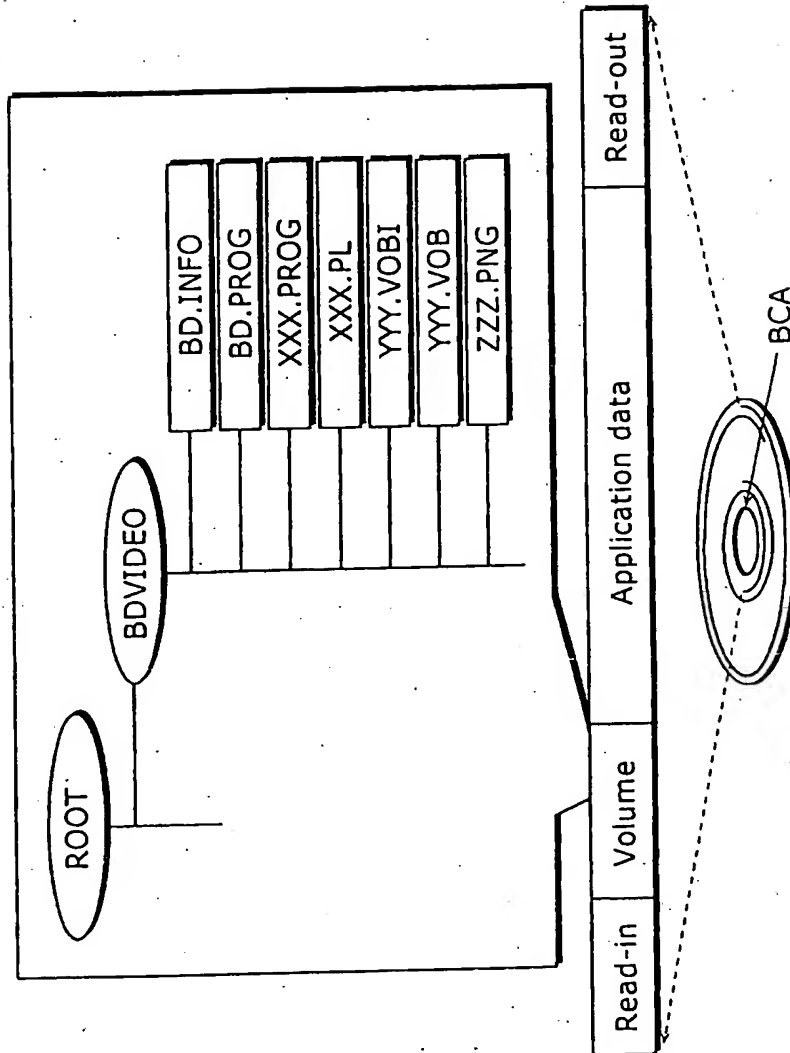


FIG. 33

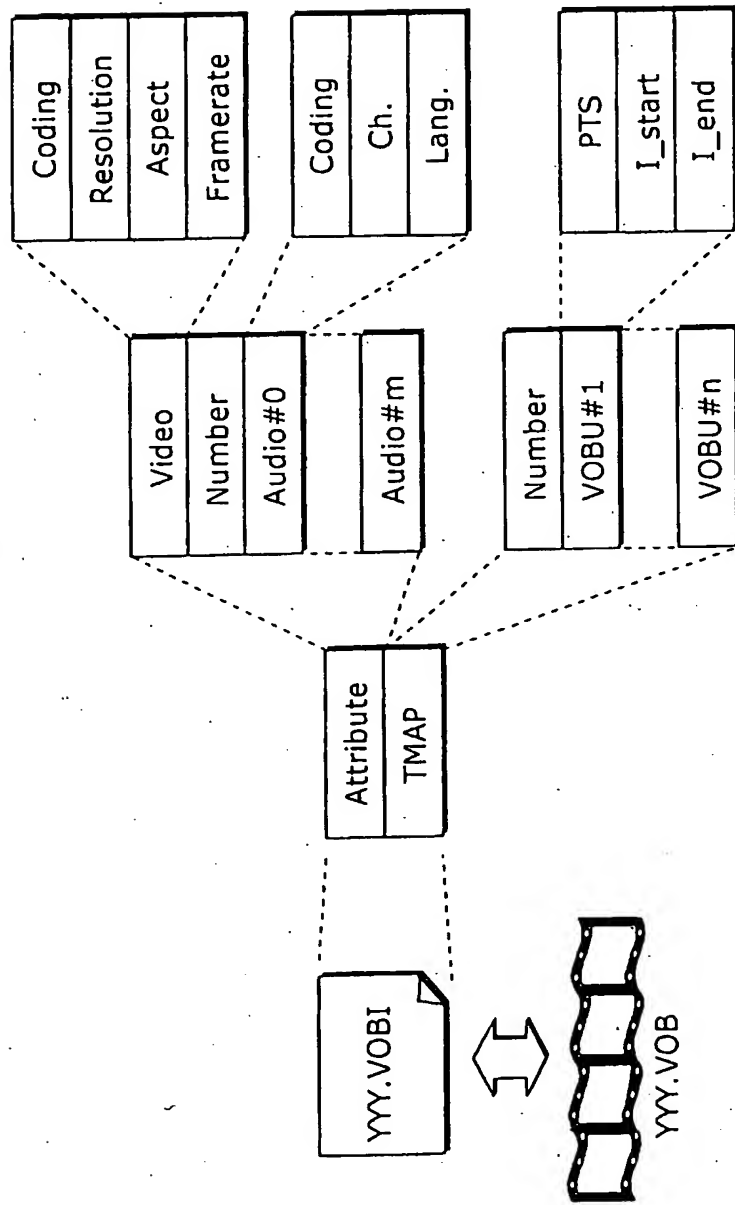
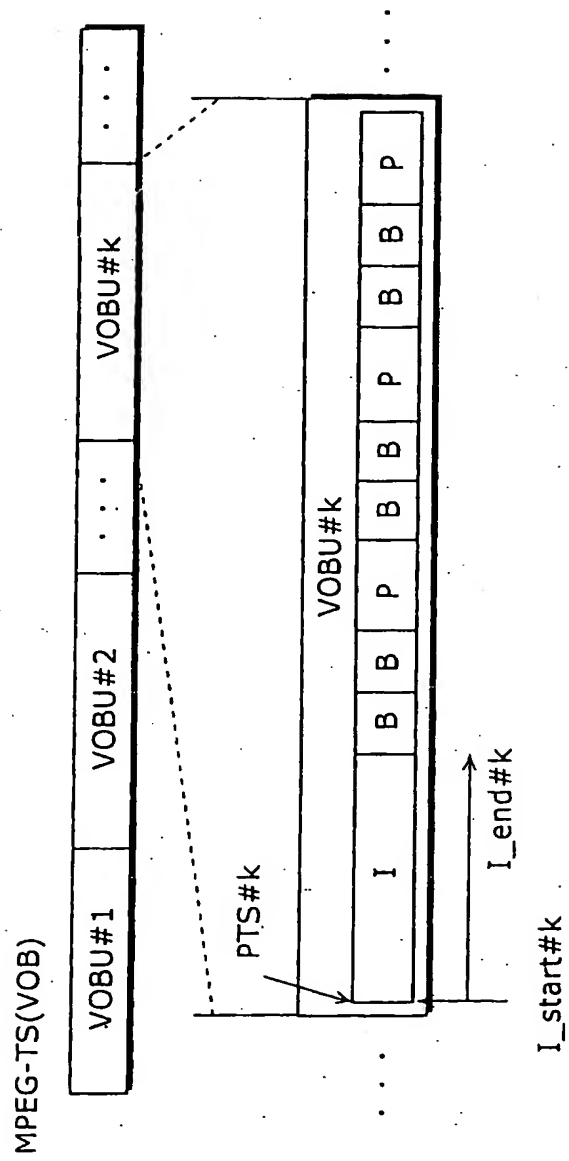


FIG. 34



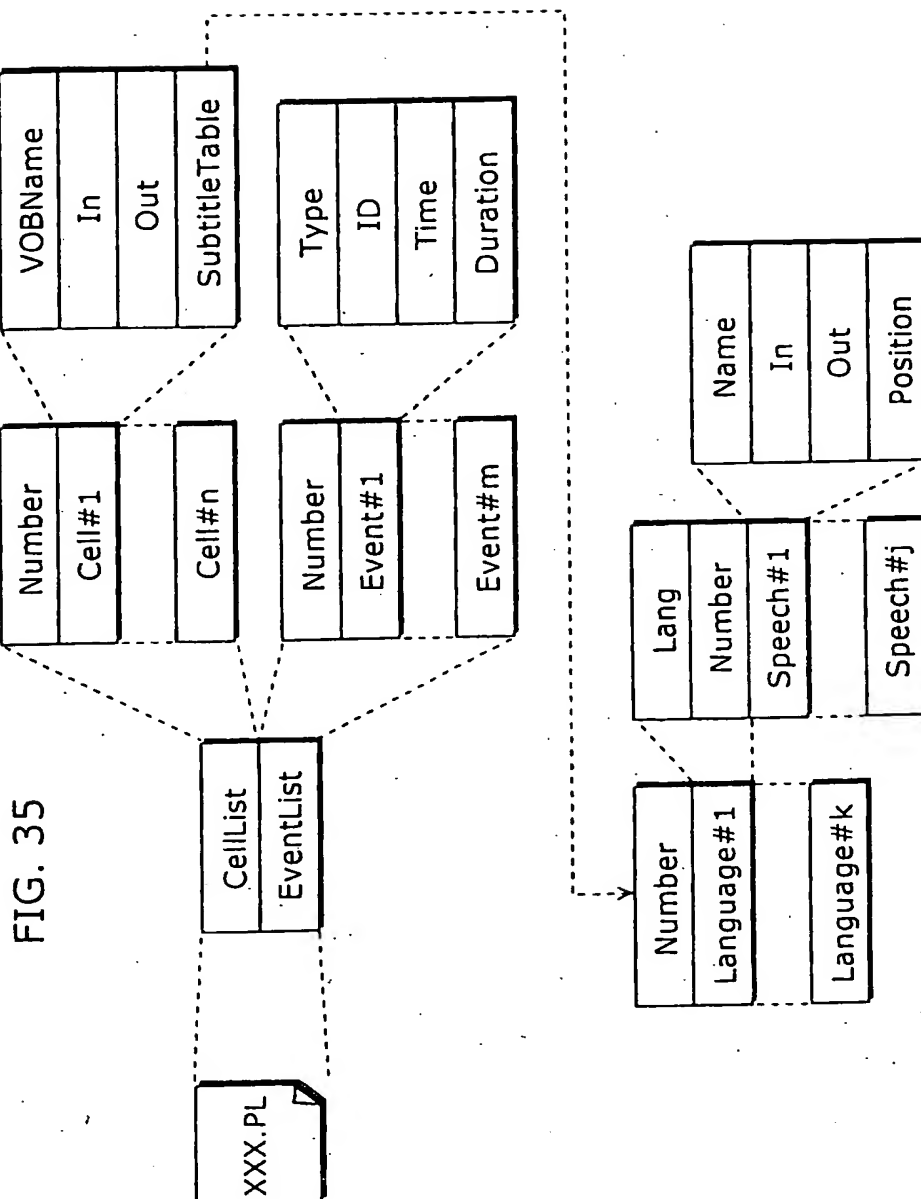


FIG. 36

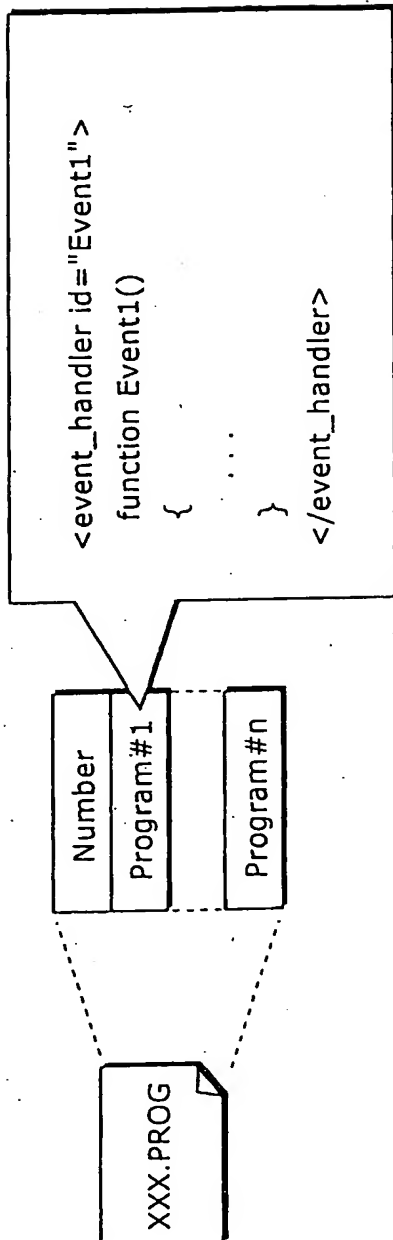


FIG. 37

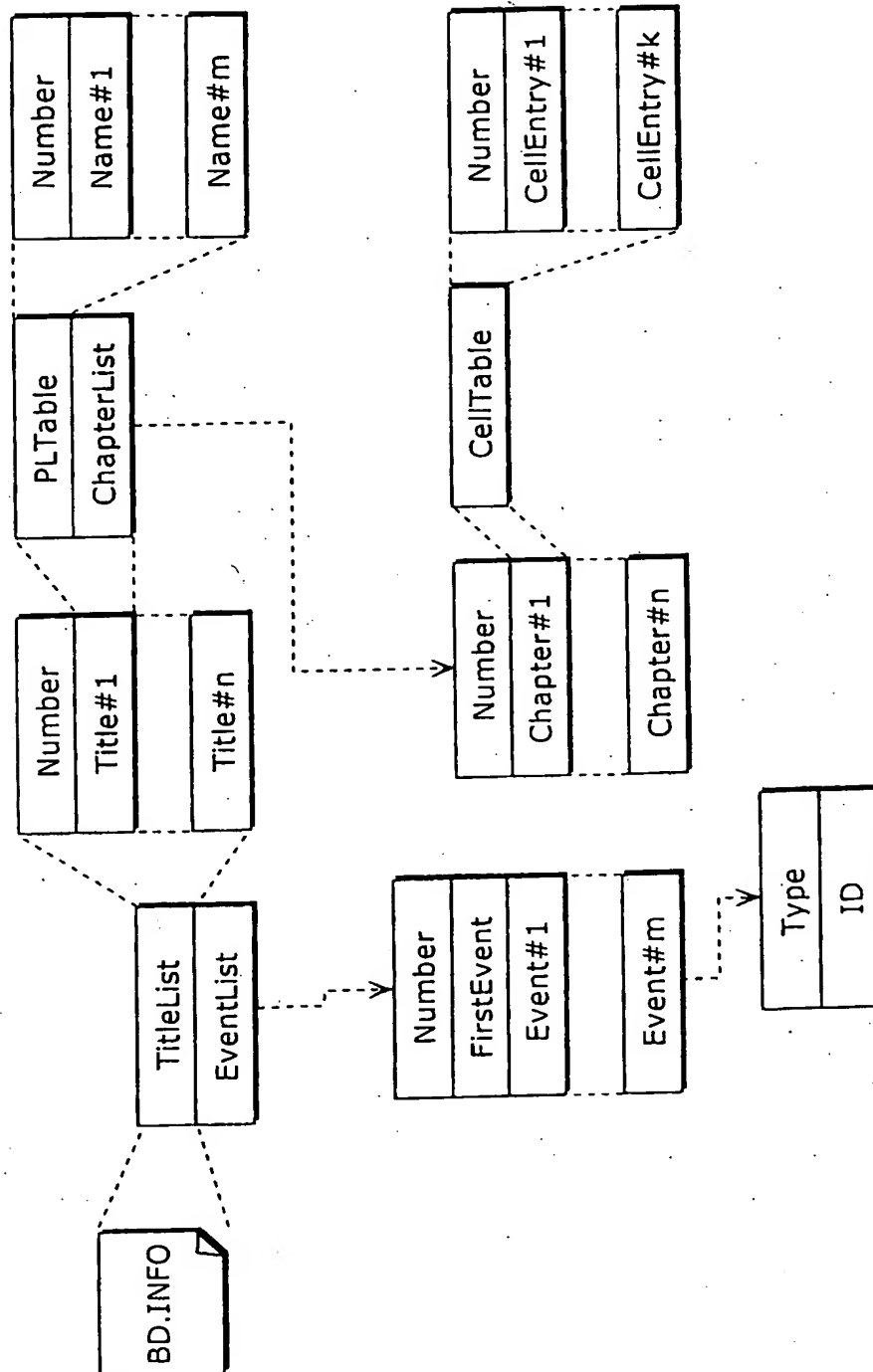


FIG. 38

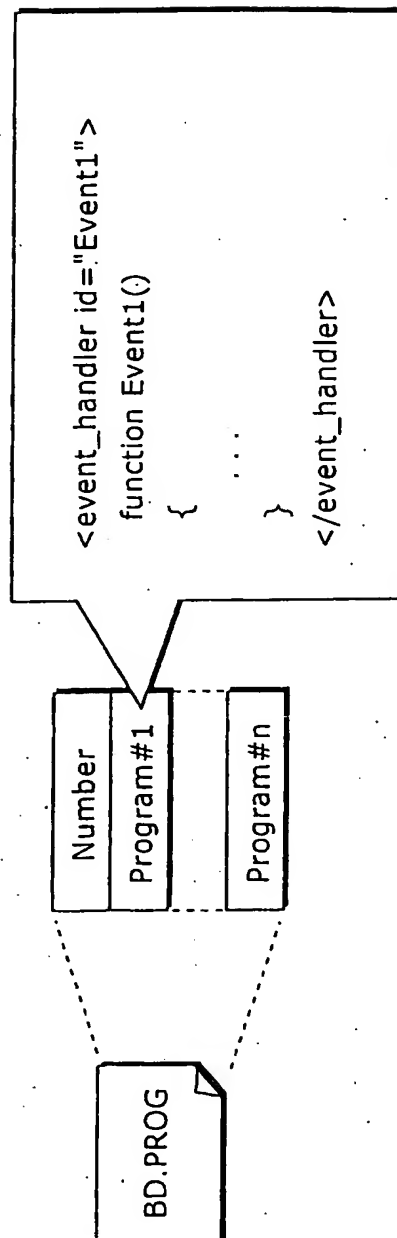


FIG. 39

